Product data sheet

1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a 4 bumps Wafer Level Chip-Size Package (WLCSP) using Trench MOSFET technology.

2. Features and benefits

- Low threshold voltage
- Ultra small package: 0.78 × 0.78 × 0.35 mm
- Trench MOSFET technology
- ElectroStatic Discharge (ESD) protection > 2 kV HBM

3. Applications

- Relay driver
- High-speed line driver
- Low-side loadswitch
- Switching circuits

4. Quick reference data

Table 1. Qui	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	12	V
V _{GS}	gate-source voltage			-8	-	8	V
I _D	drain current	V_{GS} = 4.5 V; T_{amb} = 25 °C; t ≤ 5 s	[1]	-	-	5	А
Static characteristics							
R _{DSon}	drain-source on-state resistance	V_{GS} = 4.5 V; I_D = 3 A; T_j = 25 °C		-	57	67	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

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5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
A1	G	gate	1 2	D
A2	S	source		
B1	D	drain		G (↓ ↓ ↓ ↓ ↓
B2	S	source	в	
			Transparent top view WLCSP4 (OL- PMCM440VNE)	S 017aaa255

6. Ordering information

Table 3. Ordering in	formation					
Type number	Package					
	Name	Description	Version			
PMCM440VNE	WLCSP4	WLCSP4: wafer level chip-size package; 4 bumps (2 x 2)	OL- PMCM440VNE			

7. Marking

Table 4. Marking codes Type number Marking code PMCM440VNE Μ 2 PIN A1 INDICATION MARKING CODE 1 (EXAMPLE) в А aaa-012880 Fig. 1. WLCSP4 marking code description

8. Limiting values

Table 5.Limiting values

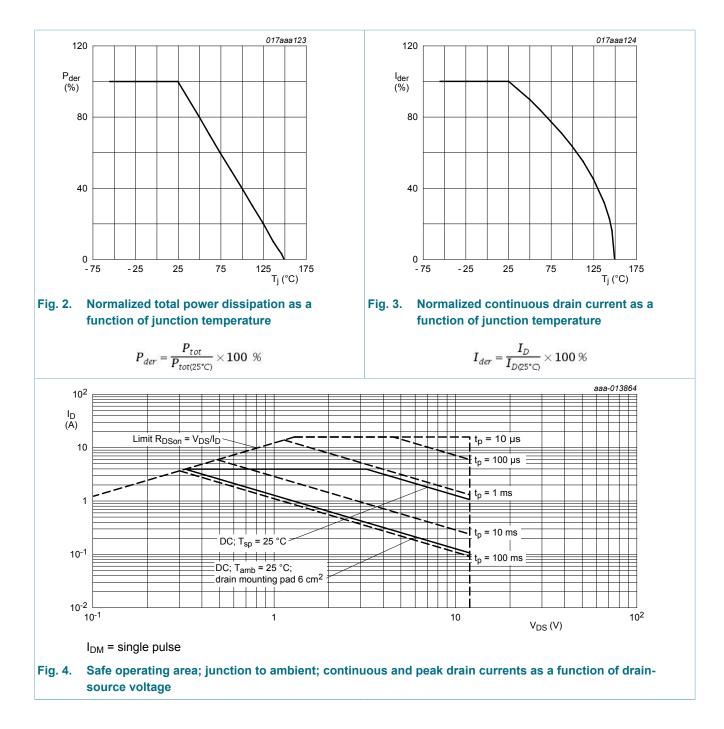
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit	
V _{DS}	drain-source voltage	T _j = 25 °C		-	12	V	
V _{GS}	gate-source voltage			-8	8	V	
I _D	drain current	V_{GS} = 4.5 V; T_{amb} = 25 °C; t ≤ 5 s	[1]	-	5	А	
		V _{GS} = 4.5 V; T _{amb} = 25 °C	[1]	-	3.9	А	
		V _{GS} = 4.5 V; T _{amb} = 100 °C	[1]	-	15.5	А	
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	16	А	
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	400	mW	
			[1]	-	1300	mW	
		T _{sp} = 25 °C		-	12500	mW	
Tj	junction temperature			-55	150	°C	
T _{amb}	ambient temperature			-55	150	°C	
T _{stg}	storage temperature			-65	150	°C	
Source-dra	Source-drain diode						
I _S	source current	T _{amb} = 25 °C	[1]	-	1.1	А	

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

[2] Device mounted on an FR4 Printed Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

12 V, N-channel Trench MOSFET



9. Thermal characteristics

Table 6.	Thermal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	R _{th(j-a)} thermal resistance from junction to ambient	in free air	[1]	-	250	300	K/W
			[2]	-	70	85	K/W
			[3]	-	85	100	K/W
		in free air; t ≤ 5 s	[3]	-	50	60	K/W
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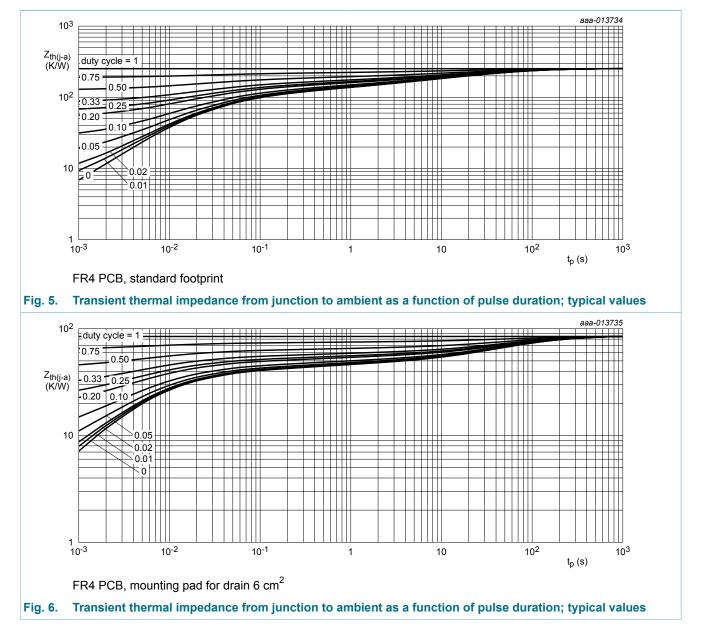
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Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
R _{th(j-sp)}	thermal resistance from junction to solder point		-	5	10	K/W

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain, 4-layer, 1 cm². [3]

Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².



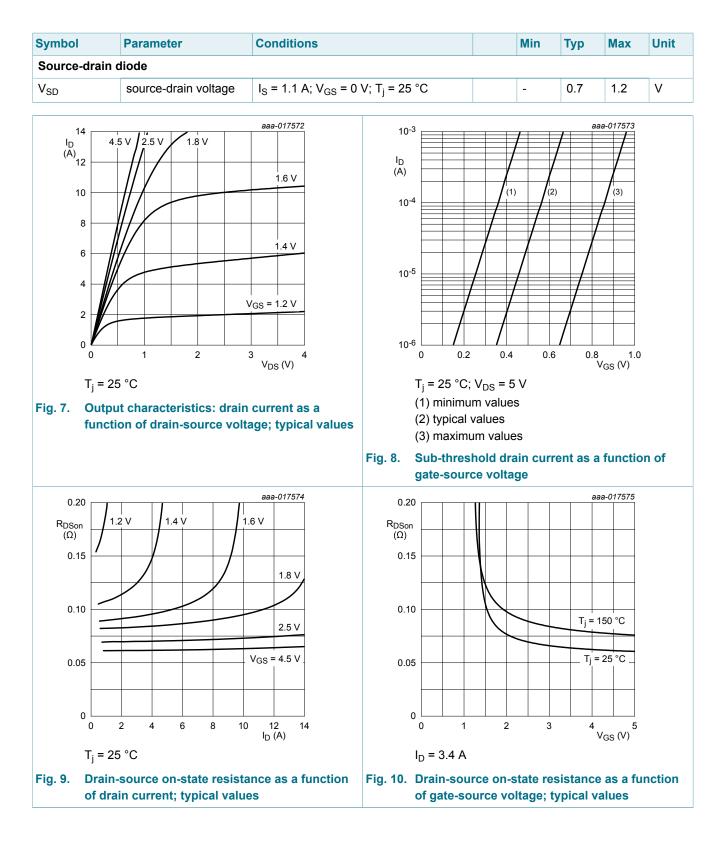
10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
Static chara	acteristics		<u> </u>			
V _{(BR)DSS}	drain-source breakdown voltage	I_D = 250 µA; V_{GS} = 0 V; T_j = 25 °C	12	-	-	V
V _{GSth}	gate-source threshold voltage	I _D = 250 μA; V _{DS} = V _{GS} ; T _j = 25 °C	0.4	0.6	0.9	V
I _{DSS}	drain leakage current	V _{DS} = 12 V; V _{GS} = 0 V; T _j = 25 °C	-	-	1	μA
I _{GSS}	gate leakage current	V_{GS} = 8 V; V_{DS} = 0 V; T_j = 25 °C	-	-	10	μA
		V_{GS} = -8 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-10	μA
		V_{GS} = 4.5 V; V_{DS} = 0 V; T_j = 25 °C	-	-	5	μA
	V_{GS} = -4.5 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-5	μA	
		V_{GS} = 2.5 V; V_{DS} = 0 V; T_j = 25 °C	-	-	200	nA
	V_{GS} = -2.5 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-200	nA	
R _{DSon} drain-source on-state resistance	drain-source on-state	V_{GS} = 4.5 V; I _D = 3 A; T _j = 25 °C	-	57	67	mΩ
	resistance	V _{GS} = 4.5 V; I _D = 3 A; T _j = 150 °C	-	71	83	mΩ
	V_{GS} = 2.5 V; I _D = 3 A; T _j = 25 °C	-	66	88	mΩ	
	V _{GS} = 1.8 V; I _D = 1 A; T _j = 25 °C	-	77	110	mΩ	
		V _{GS} = 1.5 V; I _D = 0.1 A; T _j = 25 °C	-	90	130	mΩ
9 _{fs}	forward transconductance	V _{DS} = 6 V; I _D = 3 A; T _j = 25 °C	-	17	-	S
R _G	gate resistance	f = 1 MHz; T _j = 25 °C	-	5.4	-	Ω
Dynamic cł	naracteristics		II			
Q _{G(tot)}	total gate charge	V _{DS} = 6 V; I _D = 3 A; V _{GS} = 4.5 V;	-	5.5	8.2	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	0.43	-	nC
Q _{GD}	gate-drain charge	-	-	1.5	-	nC
C _{iss}	input capacitance	V_{DS} = 6 V; f = 1 MHz; V_{GS} = 0 V;	-	360	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	160	-	pF
C _{rss}	reverse transfer capacitance	-	-	140	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = 6 V; I _D = 3 A; V _{GS} = 4.5 V;	-	6.3	-	ns
t _r	rise time	R _{G(ext)} = 6 Ω; T _j = 25 °C	-	24	-	ns
t _{d(off)}	turn-off delay time		-	27	-	ns
t _f	fall time	1		17	-	ns

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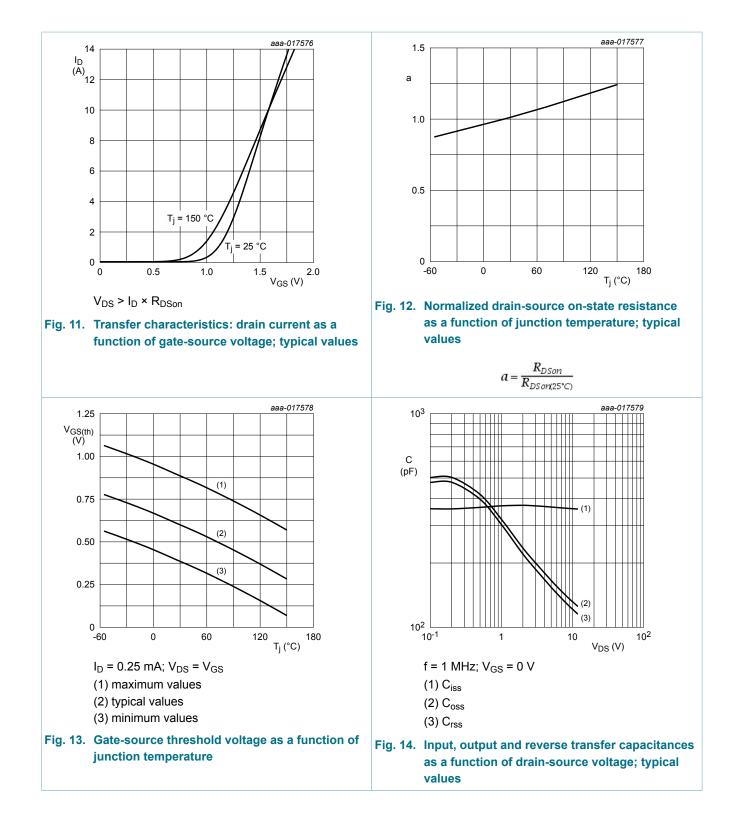
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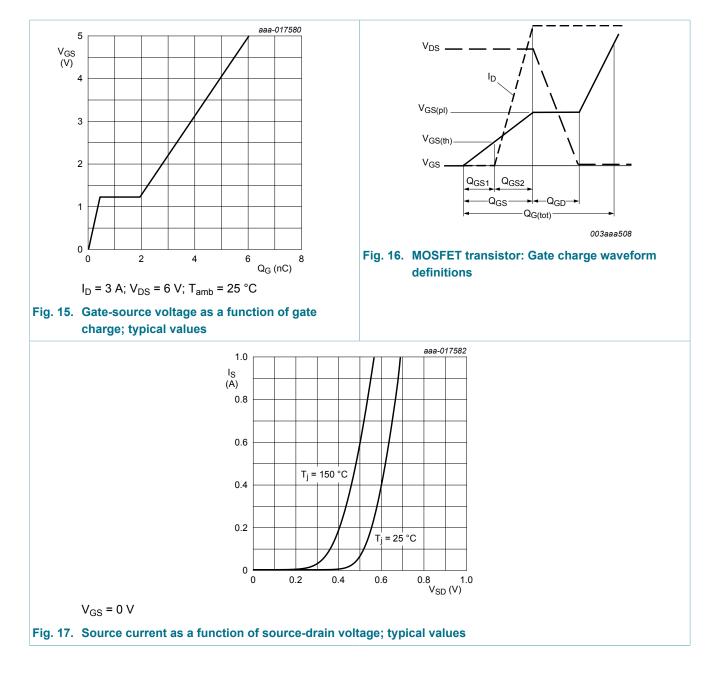
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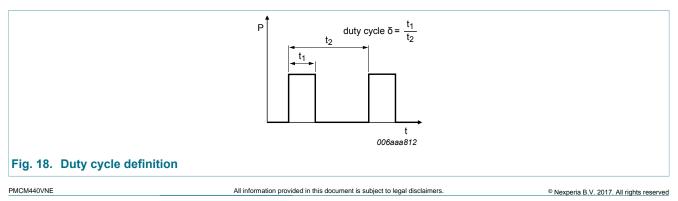
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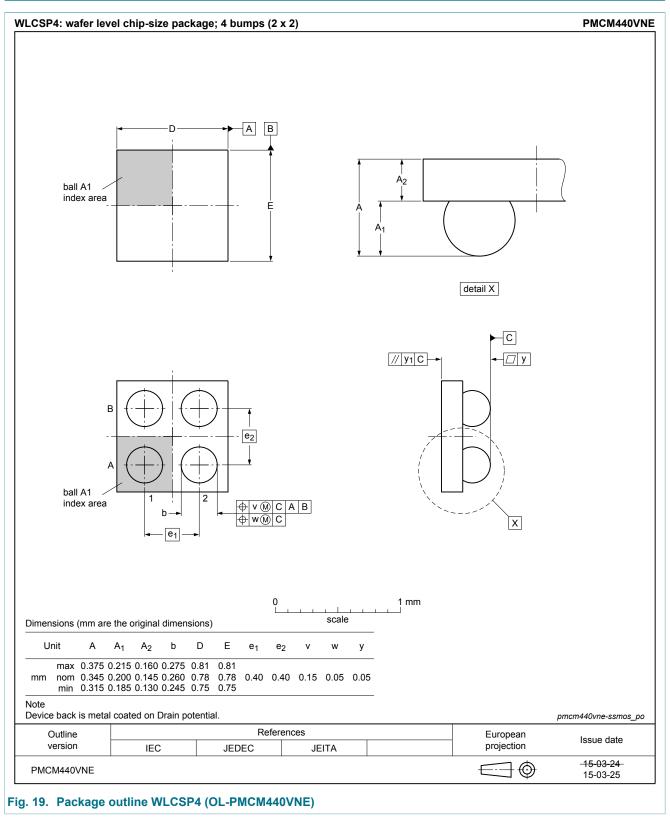


11. Test information



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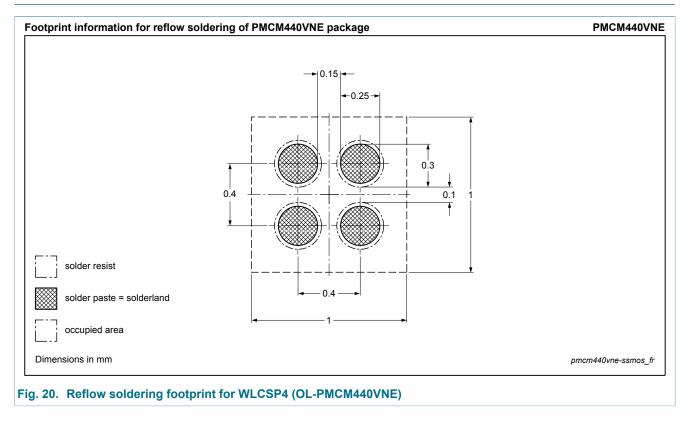
12. Package outline



PMCM440VNE

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13. Soldering



14. Revision history

Table 8. Revision his	ble 8. Revision history					
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMCM440VNE v.1	20150407	Product data sheet	-	-		

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15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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