Vishay Semiconductors

AAP Gen 7 (TO-240AA) Power Modules Standard Diodes, 80 A



www.vishay.com

AAP	Gan	7 (τ∩₋	240	Δ	Δ

PRIMARY CHARACTERISTICS					
I _{F(AV)}	80 A				
Type	Modules - Diode, High Voltage				
Package	AAP Gen 7 (TO-240AA)				
Circuit configuration	Two diodes doubler circuit, two diodes common cathode, two diodes common anode, single diode				

MECHANICAL DESCRIPTION

The AAP Gen 7 (TO-240AA), new generation of AAP module, combines the excellent thermal performances obtained by the usage of exposed direct bonded copper substrate, with advanced compact simple package solution and simplified internal structure with minimized number of interfaces.

FEATURES

- · High voltage
- Industrial standard package



- · Low thermal resistance
- UL approved file E78996
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

BENEFITS

- Excellent thermal performances obtained by the usage of exposed direct bonded copper substrate
- Up to 1600 V
- · High surge capability
- · Easy mounting on heatsink

ELECTRICAL DESCRIPTION

These modules are intended for general purpose high voltage applications such as high voltage regulated power supplies, lighting circuits, temperature and motor speed control circuits, UPS and battery charger.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
1		80	A			
I _{F(AV)}	T _C	110	°C			
I _{F(RMS)}		126				
I _{FSM}	50 Hz	1500	Α			
	60 Hz	1570				
l ² t	50 Hz	11.25	kA ² s			
	60 Hz	10.26	KA-S			
l ² √t		112.5	kA²√s			
V _{RRM}	Range	400 to 1600	V			
T _{Stg} , T _J		-40 to +150	°C			

Vishay Semiconductors

ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS							
TYPE NUMBER	VOLTAGE CODE	V _{RRM} , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} MAXIMUM AT T _J = 150 °C mA			
	04	400	500				
	06	600	700				
	08	800	900				
VS-VSK.71	10	1000	1100	10			
	12	1200	1300				
	14	1400	1500				
	16	1600	1700				

FORWARD CONDUCTION						
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNITS
Maximum average forward current at case temperature	I _{F(AV)}	180° condu	ction, half sine	wave	80 110	A °C
Maximum RMS forward current	I _{F(RMS)}				126	
		t = 10 ms	No voltage		1500	
Maximum peak, one-cycle forward,	I	t = 8.3 ms	reapplied		1570	Α
non-repetitive surge current	I _{FSM}	t = 10 ms	100 % V _{RRM}		1260	
		t = 8.3 ms reapplied Sinusoidal half wave,	1320			
	l ² t	t = 10 ms	No voltage	initial T _J = T _J maximum	11.25	kA ² s
Maximum I ² t for fusing		t = 8.3 ms	reapplied		10.26	
I Waxiindiii i-t ioi idsiiig		t = 10 ms	100 % V _{RRM}		7.95	
		t = 8.3 ms	reapplied		7.23	
Maximum I ² √t for fusing	I ² √t	t = 0.1 ms t	o 10 ms, no vol	tage reapplied	112.5	kA ^{2√} s
Low level value of threshold voltage	V _{F(TO)1}	(16.7 % x π	(16.7 % x π x $I_{F(AV)}$ < I < π x $I_{F(AV)}$), $T_J = T_J$ maximum			V
High level value of threshold voltage	V _{F(TO)2}	$(I > \pi \times I_{F(AV)})$	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$		0.83	V
Low level value of forward slope resistance	r _{f1}	(16.7 % x π x $I_{F(AV)}$ < I < π x $I_{F(AV)}$), $T_J = T_J$ maximum			3.22	mΩ
High level value of forward slope resistance	r _{f2}	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$			2.89	11152
Maximum forward voltage drop	V_{FM}	$I_{FM} = \pi \times I_{F(A)}$	_{AV)} , T _J = 25 °C,	t _p = 400 μs square wave	1.6	V

BLOCKING							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum peak reverse leakage current	I _{RRM}	T _J = 150 °C	10	mA			
Maximum RMS insulation voltage	V _{INS}	50 Hz	3000 (1 min) 3600 (1 s)	V			



www.vishay.com

Vishay Semiconductors

THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER	METER SYMBOL TEST CONDITIONS		VALUES	UNITS			
Junction and storage temp	erature range	T _J , T _{Stg}		-40 to +150	°C		
Maximum internal thermal resistance, junction to case per leg		R _{thJC}	DC operation	0.28	°C/W		
Typical thermal resistance, case to heatsink per module		R _{thCS}	Mounting surface flat, smooth and greased	0.1	C/VV		
	to heatsink		A mounting compound is recommended and the	4			
Mounting torque ± 10 %	busbar		torque should be rechecked after a period of 3 hours to allow for the spread of the compound.	3	Nm		
Approximate weight				75	g		
Approximate weight				2.7	OZ.		
Case style			JEDEC®	AAP Gen 7	(TO-240AA)		

△R CONDUCTION PER JUNCTION											
DEVICES	8	SINE HALF	WAVE CO	NDUCTION	١	RECTANGULAR WAVE CONDUCTION				LINUTC	
DEVICES	180°	120°	90°	60°	30°	180°	120°	90°	60°	30°	UNITS
VSK.71	0.075	0.088	0.113	0.155	0.228	0.06	0.094	0.12	0.158	0.23	°C/W

Note

Table shows the increment of thermal resistance R_{thJC} when devices operate at different conduction angles than DC

www.vishay.com

Vishay Semiconductors

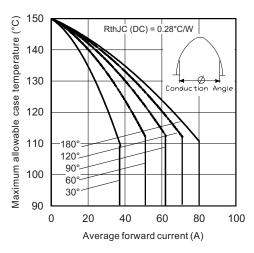


Fig. 1 - Current Ratings Characteristics

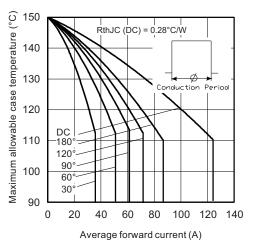


Fig. 2 - Current Ratings Characteristics

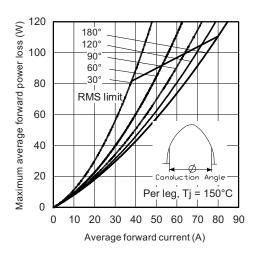


Fig. 3 - Forward Power Loss Characteristics

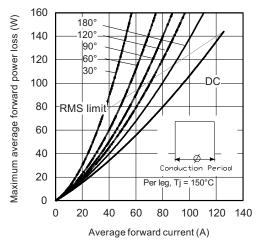
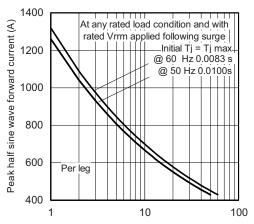


Fig. 4 - Foward Power Loss Characteristics



Number of equal amplitude half cycle current pulses (N)

Fig. 5 - Maximum Non-Repetitive Surge Current

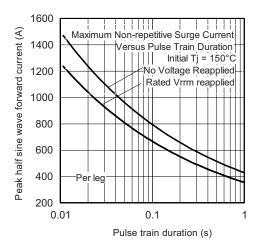


Fig. 6 - Maximum Non-Repetitive Surge Current

www.vishay.com

Vishay Semiconductors

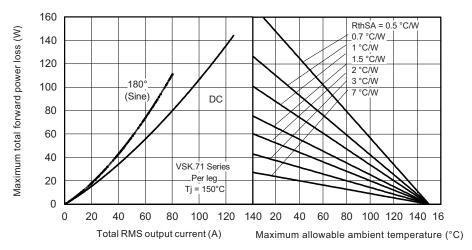


Fig. 7 - Forward Power Loss Characteristics

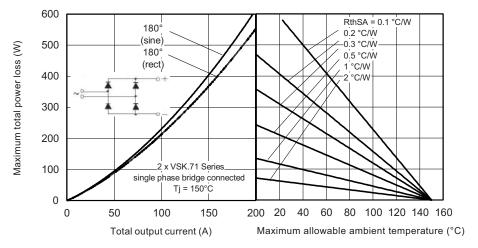


Fig. 8 - Forward Power Loss Characteristics

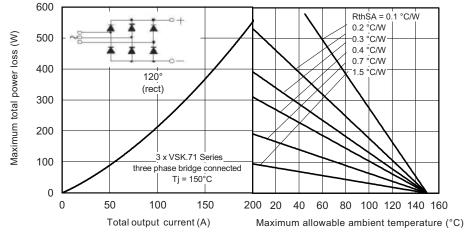


Fig. 9 - Forward Power Loss Characteristics

Vishay Semiconductors

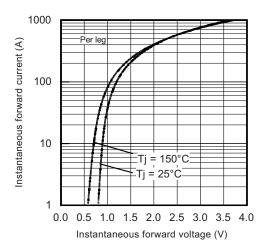


Fig. 10 - Forward Voltage Characteristics

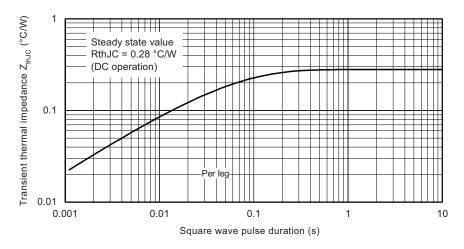
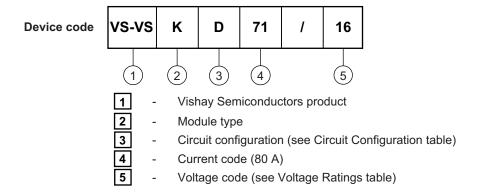


Fig. 11 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE



Note

• To order the optional hardware go to www.vishay.com/doc?95172

www.vishay.com

Vishay Semiconductors

CIRCUIT CONFIGURATION		
CIRCUIT DESCRIPTION	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING
Two diodes doubler circuit	D	VSKD (1) 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
Two diodes common cathode	С	VSKC (1) - (2) - (3)
Two diodes common anode	J	VSKJ (1) 0
Single diode	E	VSKE (1) 0

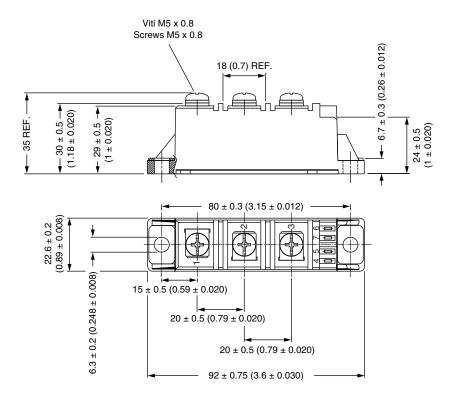
LINKS TO RELAT	
Dimensions	www.vishay.com/doc?95369



Vishay Semiconductors

ADD-A-PAK Generation VII - Diode

DIMENSIONS in millimeters (inches)





Legal Disclaimer Notice

Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.